



Asteralabs to Participate in the Citi 2026 Global TMT Conference

September 1, 2026

SAN JOSE, Calif., Sept. 01, 2026 (GLOBE NEWSWIRE) -- Astera Labs, Inc. (Nasdaq: ALAB), a leader in semiconductor-based connectivity solutions for rack-scale AI infrastructure, today announced that it will participate in the Citi 2026 Global TMT Conference on Sept. 9, 2026. Astera Labs' presentation is scheduled for 8:50 am ET. A webcast of the session will be made available on Astera Labs' investor relations website at <https://ir.asteralabs.com>.

About Astera Labs

Asteralabs (Nasdaq: ALAB) provides rack-scale AI infrastructure through purpose-built connectivity solutions. By collaborating with hyperscalers and ecosystem partners, Astera Labs enables organizations to unlock the full potential of modern AI. Astera Labs' Intelligent Connectivity Platform integrates CXL[®], Ethernet, NVLink Fusion, PCIe[®], and UALink[™] semiconductor-based technologies with the company's COSMOS software suite to unify diverse components into cohesive, flexible systems that deliver end-to-end scale-up and scale-out connectivity. The company's custom connectivity solutions business complements its standards-based portfolio, enabling customers to deploy tailored architectures to meet their unique infrastructure requirements. Discover more at www.asteralabs.com.

IR CONTACT: Leslie Green
leslie.green@asteralabs.com

